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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE #4

In re application of: Adam Jacobs

6 Serial No.: 10/071277

Group No.:

Filed: 2/8/02

Examiner:

For: Integrated CMOS imaging array and dark current monitor

11 Commissioner of Patents and Trademarks
Washington, D.C. 20231

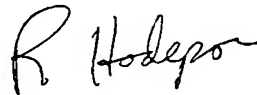
INFORMATION DISCLOSURE STATEMENT

16

The attached form 1449 (Modified) gives references pertaining to the above application.

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Respectfully,

26 914-762-5248 (Fax 914-762-4126)
E-MAIL - patents@aip.orgRodney T. Hodgson Agent # 37,849

24295

PATENT TRADEMARK OFFICE

Sheet 1 of 1

Form PTO 1449 (modified)				Docket No 901-010		Serial No 10/071277	
LIST OF PRIOR ART CITED BY APPLICANT				Applicant: Adam Jacobs			
				Filing Date 2/8/02		Group	
U. S. PATENT DOCUMENTS							
Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date
	AA	6101287	8/8/00	Corum; Curtis A.			
	AB	4703442	10/27/87	Levine; Peter A.			
	AC	4200934	4/29/80	Hofmann; Otto			
FOREIGN PATENT DOCUMENTS							
Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date
	AD						
OTHER PRIOR ART (Including Author, Title, Date, Pertinent Pages, Etc.)							
	AE						
	AF						
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Examiner				Date Considered			
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant							